



Birla Institute of Technology & Science, Pilani

Pilani Campus

Department of Electrical & Electronics Engineering

BITS EEE CON 2022

International Conference on Next Generation Systems and Networks

4 - 5 November, 2022

About BITS Pilani

Birla Institute of Technology & Science, Pilani (BITS Pilani), declared a Deemed to be University in 1964, was conferred the distinction of Institution of Eminence in 2020 by the Ministry of Education, Govt. of India. The qualities of innovation, enterprise, commitment to excellence, adherence to merit, and transparency, have characterized the Institute during its inexorable march to eminence.

Over the span of 58 years, across its campuses, the Institute has been offering educational programmes that are academically rigorous, broad-based, modular, flexible, and relevant to the needs of industry and society, with the agility to adapt to the changing environment, at all three levels, namely, Integrated First Degree, Higher Degree and the Doctoral Degree. The institute has taken pioneering initiatives towards the development of institutionalized linkages with industry, through its (i) Practice school, (ii) Technology Innovation Center, and (iii) Off-campus Work Integrated Learning Programmes (WILP).

BITS Pilani-Pilani campus is spread over 328 acres, the campus is self-contained, and houses all the amenities and buildings that befit an institute of international standards. An intellectual powerhouse, the campus has successfully attracted healthy amounts of research funding from government grants and private bodies. The research facilities are wide-ranging, with a number of laboratories focused on all aspects of science, engineering, and technology.

About Department

The Department of Electrical & Electronics Engineering, which includes EEE, ECE and EIE streams, is one of the largest and the oldest departments on the campus. The department has earned a very high reputation in the country for the various degree programmes offered and also has established collaborative linkages with many institutions in the country and the universities abroad. The department emphasizes interdisciplinary and industrial research and collaborations to advance knowledge and educate students to best serve the nation and the world in the coming times.

About Conference

This conference aims to provide some reliable and efficient design solutions for the next generation networks and systems. The concept of smart networks which encompasses various intelligent networks like the concept of smart grids and autonomous vehicles, to name a few, have led to various data efficient algorithm designs. Use of artificial intelligence to take the processing from the devices to the central servers along with the cloud based architecture design has provided many solutions for optimizing the power consumption and made user devices intelligent. The conference will enable a platform for researchers, scientists, engineers, policy makers and practitioners globally to present their latest research findings and ideas which fall under the scope of BITS-EEE-CON.

Call for Papers

Participants from Academia, R&D Organizations, and Industries are invited to submit original and unpublished full research papers in both theoretical and experimental domains on the following and other related topics.

Energy, Power and Control

- Renewable and Green Energy systems and their management, Smart Grid;
- Energy Storage Devices and Charging infrastructure;
- Industrial Electronics, Instrumentation, Fault diagnosis and Sensing Systems;
- Unmanned & Intelligent Transportation, Electrical Vehicles;
- Robotics and Automation;

Communication and Signal Processing

- Image, Video, and Biomedical Signal Processing;
- AI for Signal Processing and Communication Networks;
- 5G and Beyond Communication and Networks;
- Cooperative, D2D Communications, Blockchain and IoT;
- Reconfigurable Intelligent Surfaces, Antenna and Propagation;

Electronics and Nanotechnology

- Electronic Materials and Devices;
- Circuits and Systems;
- Consumer and Flexible Electronics;
- Micro and Nanoelectronics;
- Photonics

Important Dates

Starting Date for Submission of Full Paper	1 March 2022
Closing of Submission of Full Paper	30 June 2022 15 Aug 2022
Notification of Acceptance of Paper	31 August 2022
Submission of Camera Ready Paper	30 September 2022
Registration Open for all	1 October 2022
Last Date of Registration of Author Category	15 October 2022
Conference Dates	4 - 5 November 2022

Publications Accepted and presented papers will be considered for publication in Scopus Indexed Springer book series "Lecture Notes in Networks and Systems".



Extended version of the accepted and presented papers will be submitted for possible publication in the following journals:

- Internet of Things and Cyber-Physical Systems
- International Journal of Multimedia Intelligence and Security

Registration Fees

	Indian Nationals		Foreign Nationals	
	Early	Late	Early	Late
Academicians	₹7000	₹8000	\$250	\$300
Research Scholars/Students	₹5000	₹6000	\$200	\$250
Industrial Personal	₹9000	₹10000	\$350	\$400

Sponsorship

TYPE	AMOUNT	PRIVILEGE
Platinum	₹1,00,000	Name and logo to appear in all advertisements, conference Website and conference kits. Special acknowledgment during inaugural and concluding functions. One half-page color advertisement in the Souvenir. Three complimentary registrations.
Gold	₹75,000	Logo to appear in all advertisements, conference website and conference kits. One half-page color advertisement in the Souvenir. Two complimentary registrations.
Silver	₹50,000	Logo to appear in all advertisements, conference website. One half-page color advertisement in the Souvenir. One complimentary registration.

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